Amendments to the Claims:

This listing of Claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-17. (Cancelled)

18. (Previously Presented) A mini cell header reading device for extracting, from a user data channel user data part of an individual connection, comprising a shift register into which a bit stream of the user data channel is shifted in synchronism with clock pulses, a first counter counting a size of a cell indicating field in a header of the mini cell shifted into the shift register in synchronism with said clock pulses, a latch register connected to the first counter and to the shift register to latch an information resident in the cell size indicating field as counted by the first counter, a memory connected to said latch register, a second counter connected to the latch register and a memory for controlling a multiplexor so that the user data part of the mini cell in the shift register is extracted from said user data bit stream, wherein said information in the latch register is used as address to the memory, and that at said address the size of the user data part is stored.

19. (Previously Presented) A cell header reading device in accordance with claim 18 wherein that said memory is a ROM memory in which there is mapped at each address received from said latch register an individual cell size.

20. (Previously Presented) A cell header reading device in accordance with claim 19, wherein there is a control system for controlling set up and release of connections in a mobile telephone system wherein said memory is a RAM memory in which said control system writes at each address received from said latch register an individual cell size.

21. (Currently Amended) A mobile telephone system comprising an ATM network to which a sending unit and a receiving unit are connected over a respective link, said sending unit comprising:

means for multiplexing mini cells from user data sources into a user data stream, said receiving unit receiving the user data stream from said ATM network, said user data stream comprising mini cells which belong to connections that are to be terminated by user data ~~links~~ sinks connected to the ~~receiving device~~ receiving unit, wherein said receiving unit comprises a first cell header reading device;

wherein said sending unit comprises a second cell header reading device; and

wherein said first cell header reading device comprises a shift register into which said user data stream is shifted in synchronism with clock pulses, a first counter counting size of a cell size indicating field in header of the mini cell shifted into the shift register in synchronism with said clock pulses, a latch register connected to the first counter and to the shift register to latch the information resident in the cell size indicating field as counted by the first counter, a memory connected to said latch register, a second counter connected to the latch register and the memory for controlling a multiplexor so that the user part of the mini cell in the shift register is extracted from said data stream, wherein the information in the latch register is used as address to the memory, and that at said address the size of the user data part is stored.

22-23. (Cancelled)

24. (Currently Amended) A mobile telephone system in accordance with ~~claim 23~~ claim 21 wherein said memory is a ROM memory in which there is mapped at each address received from said latch register an individual cell size.

25. (Previously Presented) A mobile telephone system in accordance with claim 24, wherein there is a control system for controlling set up and release of connections in the mobile telephone system wherein said memory is a RAM memory in which said

control system writes at each address received from said latch register an individual cell size.